



Drafts

Pending

Active

- ☒ L1: (1009) ((dynamic near3 mode) and
- ☒ L2: (2) ((dynamic near3 mode) with (
- ☒ L3: (39) ((static near3 mode)with (p
- ☒ L4: (0) ((static near3 mode) same (not

Failed

Saved

- ☒ (22) ((array bank block) and ((signal
- ☒ (4) ((test\$3 with dedicat\$3 with line
- ☒ (4) ((array bank block) and ((signal
- ☒ (22) ((static adj2 operation) ((static adj2 mode)) near5 ((precharg\$3 pre-charg\$3))
- ☒ (4) ((dynamic adj2 operation) ((dynat
- ☒ (67) ((dynamic adj2 operation) ((dynat

Favorites

Tagged (17)

UDC

Queue

Browse

Queue

Clear

DB: USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_IDB

Default operator: DR

☒ Plural☒ Highlight all the terms initially

((static near3 mode)with ((precharg\$3 pre-charg\$3))

BRS form

S&R form

Image

Text

HTML

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20020196057 A1	20021226	123	Data output circuit with reduced output noise	327/108		
2	<input type="checkbox"/>	<input type="checkbox"/>	US 20020084835 A1	20030704	23	Semiconductor device with reduced leakage of current	327/544		
3	<input type="checkbox"/>	<input type="checkbox"/>	US 20020067647 A1	20030606	12	Semiconductor integrated circuit device	365/203		
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6563730 B1	20030513	12	Low power static RAM architecture	365/154	365/189.01	
5	<input type="checkbox"/>	<input type="checkbox"/>	US 6445222 B1	20020903	113	Data output circuit with reduced output noise	327/108	328/87; 327/112	
6	<input type="checkbox"/>	<input type="checkbox"/>	US 6163180 A	20001219	112	Data output circuit with reduced output noise	327/112	327/540; 327/541	
7	<input type="checkbox"/>	<input type="checkbox"/>	US 5948081 A	19990907	11	System for flushing queued memory write request	710/40	710/25; 710/16;	

HLS

Details

HTML

Ready

NUM

EA [09994610 wsp 1]

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(22) array bank block and signal a
 (4) test83 with address 83 with line
 (4) array bank block and signal ad
 (22) static adj2 operation static
 (4) ((dynamic adj2 operation) (dynam
 (22) dynamic adj2 operation dynam

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Browse Queue Clear
 DB: USPAT; US-PG-PUB; EPO; JPO; DERWENT; IBM; YDB
 Default operator OR
☒ Plurals
☒ Highlight all hit terms initially

(dynamic adj2 operation dynamic adj2 model near5
 (precharge93 pre-charge93) and static adj2 operation
 static adj2 model near5 precharge93 pre-charge93

☐ BRS form ☐ IS&R form ☐ Image ☐ Text ☐ HTML

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20020067647 A1	20020606	12	Semiconductor integrated circuit device	365/203		
2	<input type="checkbox"/>	<input type="checkbox"/>	US 5777491 A	19980707	21	High-performance differential cascode voltage	326/113	326/121; 326/177	
3	<input type="checkbox"/>	<input type="checkbox"/>	US 4086500 A	19780425	7	Address decoder	326/108	326/98; 365/330.06	
4	<input type="checkbox"/>	<input type="checkbox"/>	US 4079332 A	19780214	9	High gain differential amplifier	330/253	330/160	

☒ Hit ☐ Details ☐ HTML

Ready

NUM